



## PATENT APPLICATION

Sheet 1 of 1

FORM PTO-1449

LIST OF PATENTS AND PUBLICATIONS FOR  
APPLICANT'S INFORMATION DISCLOSURE  
STATEMENT

(Use several sheets if necessary)

ATTY. DOCKET NO.

10030039-1

SERIAL NO.

10/601,993

APPLICANT

Brian J. Galloway, et al.

FILING DATE

June 23, 2003

GROUP

28/6

## REFERENCE DESIGNATION

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL	*	DOCUMENT NUMBER	DATE	NAME	
<i>LMN</i>		6,055,286	Apr. 25, 2000	Wu, et al.	<i>class/sub</i> <i>375/375</i>
<i>LMN</i>					

## FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	NAME	TRANSLATION	
					YES	NO

## OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

<i>LMN</i>		POTTBACKER, Ansgar, et al. "A Si Bipolar Phase and Frequency Detector IC for Clock Extraction up to 8 Gb/s", IEEE journal of Solid-State Circuits. Vol. 27, No.12, December 1992, pp. 1747-1751.
<i>LMN</i>		RABAEY, J.M., "Digital Integrated Circuits", Prentice Hall, 1996, pp. 533-538.

EXAMINER

DATE CONSIDERED

08/02/2004

\* Copies of these references are not enclosed pursuant to 37 CFR 1.98(d). (See accompanying IDS)